HOLY MARY INSTITUTE OF TECHNOLOGY & SCIENCE

(UGC AUTONOMOUS)

(Approved by AICTE, New Delhi, and Permanent Affiliation to JNTUH, Accredited by NAAC 'A' grade)

Bogaram (V), Keesara (M), Medchal (Dist), Hyderabad, Telangana State

EXAMINATION BRANCH

I-M.Tech I- SEMESTER – HITS R21 REGULATION II- MID TERM EXAMINATIONS APRIL-2022

TIME TABLE

TIME→ FN: 10.00 AM TO 12.00 PM
AN: 2.00 PM TO 4.00 PM

BRANCH	DATE,DAY AND SESSION							
	18-04-2022 FN MONDAY	18-04-2022 AN MONDAY	19-04-2022 FN TUESDAY	19-04-2022 AN TUESDAY Professional Elective-I	20-04-2022 FN WEDNESDAY Professional Elective-II	20-04-2022 AN WEDNESDAY Audit Course - I	21-04-2022 FN THURSDAY	21-04-2022 THURSD
HIGHWAY ENGINEERING	Highway Policy & Planning	Highway Material Characterization	Evaluation of Strengthening of Pavements	Road Safety Engineering	Traffic Engineering	English for Research Paper Writing	Pavement Materials Lab	Traffic Ana Lab
ELECTRICAL POWER SYSTEMS	Power System Analysis	Economic Operation of Power Systems	Renewable Energy Systems	HVDC Transmission	Reactive Power Compensation and Management	English for Research Paper Writing	Power Systems Computation Lab-I	Advanced P Systems I
POWER ELECTRONICS	Power Electronic Converters	Machine Modeling and Analysis	Power Electronics for Renewable Energy Systems	Smart Grid Technologies	Power Semiconductor Devices and Modling	English for Research Paper Writing	Machine Modeling and Analysis Lab	Power Elect Converters
CAD/CAM	Advanced CAD	Computer Aided Manufacturing	Performance Modelling and Analysis of Manufacturing	Additive Manufacturing Technologies	Automation in Manufacturing	English for Research Paper Writing	Advanced Computer Aided Design Lab	Computer A Manufactu Lab

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EMBEDDED SYSTEMS	System Design with Embedded Linux	Microcontrollers & Programmable Digital Signal Processors	Hardware Software Co-Design	Artificial Intelligence & Machine Leaning	Advanced Computer Architecture	English for Research Paper Writing	System Design with Embedded Linux Lab	Microcon.. & Progra.. Digital .. Processo..
VLSI DESIGN	Digital Design and Verification	Microcontrollers and Programmable Digital Signal Processors	Low Power VLSI Design	Full Custom IC Design	IOT and Its Applications	English for Research Paper Writing	Digital Design and Verification Lab	Microcon.. and Program.. Digital .. Processo..
COMPUTER SCIENCE & ENGINEERING	Advanced Data Structures	Mathematical Foundations of Computer Science	Machine Learning	Advanced Artificial Intelligence	Cloud Computing	English for Research Paper Writing	Advanced Data Structures Lab	Machine L.. La..

DATE: 23-03-2022

Controller of Examinations
Holy Mary Institute of Technology & Science
CoE (UGC-Autonomous)
Bogaram (V), Keesara(M), Medchal (Dist)-501 301, T.S.

PRINCIPAL
Holy Mary Institute of Technology & Science
Principal/Chief Controller Of Examination
(UGC-Autonomous)
Bogaram (V), Keesara(M), Medchal (Dist)-501 301, T.S.